

ORAR – Anul II

Ora	Luni	Marti	Miercuri	Joi	Vineri
8 ⁰⁰ - 9 ⁰⁰	MS – <i>lab-</i> G1, R. Serban A2.9.	CIA – <i>pr-</i> G1, E.Posdarascu , B2	Tehnici de Optimizare – <i>curs-</i> (c) D.Gogoncea, B1.2 amfiteatru.	Masurari si Traductoare – <i>curs-</i> E. Posdarascu B1.2	
9 ⁰⁰ - 10 ⁰⁰					MS <i>lab,G2</i> R. Serban, A2.9
10 ⁰⁰ - 11 ⁰⁰	MS – <i>curs-</i> B1.2, Armas I.	MT – <i>lab-</i> G1, Mihailescu, A7lab	IE – <i>curs-</i> A6.9, Amf. Miclea C.	Electr. Digitala . – <i>curs-</i> E. Posdarascu B1.2	
11 ⁰⁰ - 12 ⁰⁰					MT – <i>lab-</i> G2, Mihailescu, A7lab
12 ⁰⁰ - 13 ⁰⁰	SDED – <i>curs-</i> (c) B1.2 amf., O. Calin	MT – <i>pr-</i> G1 Celestin D. ,B1lab.	TO – <i>sem-</i> G1, B,8.3 Plavitu A.	Circ. Integr. Analogice – <i>curs-</i> E. Posdarascu B1.2.	
13 ⁰⁰ - 14 ⁰⁰					MT – <i>pr-</i> G2, Celestin D. B1lab
14 ⁰⁰ - 15 ⁰⁰	SDED – <i>sem-</i> G2, B2 O.Calin	CIA – <i>lab-</i> G1 Mihailescu A7lab	ED – <i>lab-</i> G2, Celestin D. B1 lab	IE – <i>lab-</i> A2.9 Celestin D. IE – <i>pr-</i> G1,A2.9 Celestin D.	
15 ⁰⁰ - 16 ⁰⁰					SDED – <i>sem-</i> G1,B2 O.Calin
16 ⁰⁰ - 17 ⁰⁰		CIA – <i>lab-</i> G3, Mihailescu A7 lab			
17 ⁰⁰ - 18 ⁰⁰			ED – <i>lab-</i> G3, Celestin D. B1 lab		
18 ⁰⁰ - 19 ⁰⁰					
19 ⁰⁰ - 20 ⁰⁰					

CIA- Circuite integrate analogice

ED- Electronica digitala

MS- Modelare si simulare

MT – Masurari si traductoare

TO – Tehnici de optimizare

SDED – sisteme dinamice cu evenimente discrete

IE – instrumentatie electronica

Decan

Secretar

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